## **SUPPLEMENT 6-B**

## SCHEMATIC BLOCK DIAGRAMS

The Schematic Block Diagram (SBD) depicts hardware and software components and their interrelationships. They are developed at successively lower levels as analysis proceeds to define lower-level functions within higher-level requirements. These requirements are further subdivided and allocated using the Requirements Allocation Sheet (RAS). SBDs provide visibility of related system elements, and traceability to the RAS, FFBD, and other system engineering documentation. They describe a solution to the functional and performance requirements established by the functional architecture; show interfaces between the system components and other systems or subsystems; support traceability

between components and their functional origin; and provide a valuable tool to enhance configuration control. The SBD is also used to develop Interface Control Documents (ICDs) and provides an overall understanding of system operations.

A simplified SBD, Figure 6-4, shows how components and the connection between them are presented on the diagram. An expanded version is usually developed which displays the detailed functions performed within each component and a detailed depiction of their interrelationships. Expanded SBDs will also identify the WBS numbers associated with the components.

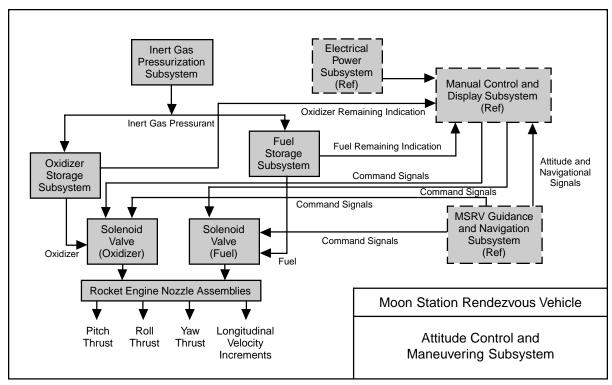


Figure 6-4. Schematic Block Diagram Example